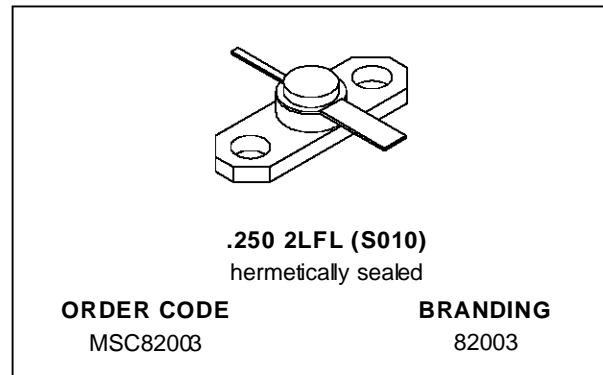


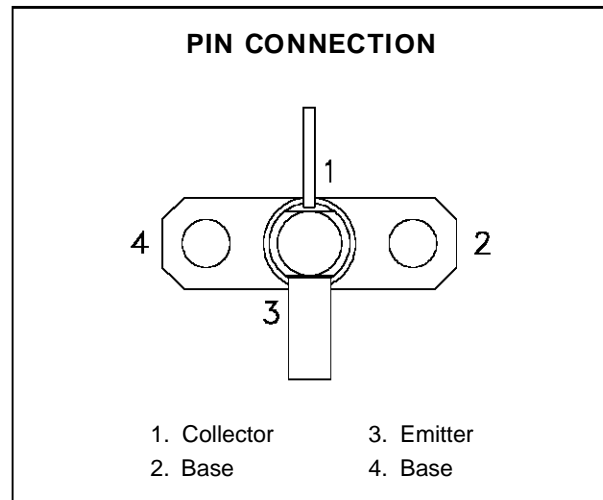
RF & MICROWAVE TRANSISTORS GENERAL PURPOSE AMPLIFIER APPLICATIONS

- EMITTER BALLASTED
- VSWR CAPABILITY $\infty:1$ @ RATED CONDITIONS
- REFRACTORY/GOLD METALLIZATION
- HERMETIC STRIPAC[®] PACKAGE
- $P_{OUT} = 3.0$ W MIN. WITH 7.8 dB GAIN @ 2.0 GHz



DESCRIPTION

The MSC82003 is a common base hermetically sealed silicon NPN microwave transistor utilizing a fishbone emitter ballasted geometry with a refractory/gold metallization system. This device is capable of withstanding an infinite load VSWR at any phase angle under rated conditions. The MSC82003 was designed for Class C amplifier applications in the 1.0 - 2.0 GHz frequency range.



ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$)

Symbol	Parameter	Value	Unit
P_{DISS}	Power Dissipation*	21.8	W
I_C	Device Current*	600	mA
V_{CC}	Collector-Supply Voltage*	35	V
T_J	Junction Temperature	200	$^{\circ}C$
T_{STG}	Storage Temperature	- 65 to +200	$^{\circ}C$

THERMAL DATA

$R_{TH(j-c)}$	Junction-Case Thermal Resistance*	8.0	$^{\circ}C/W$
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*Applies only to rated RF amplifier operation

MSC82003

ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

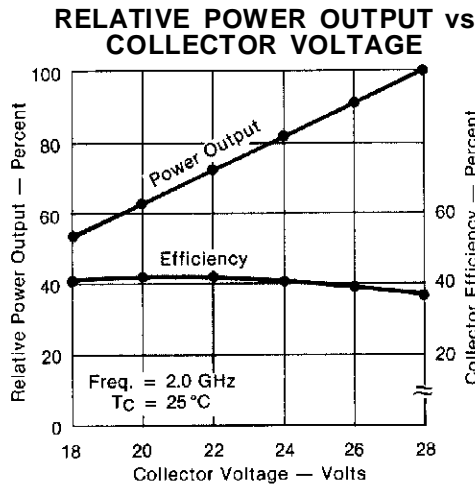
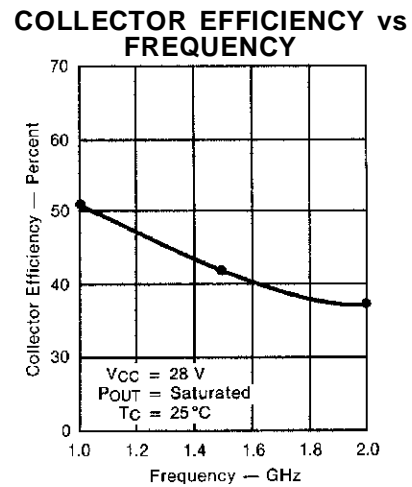
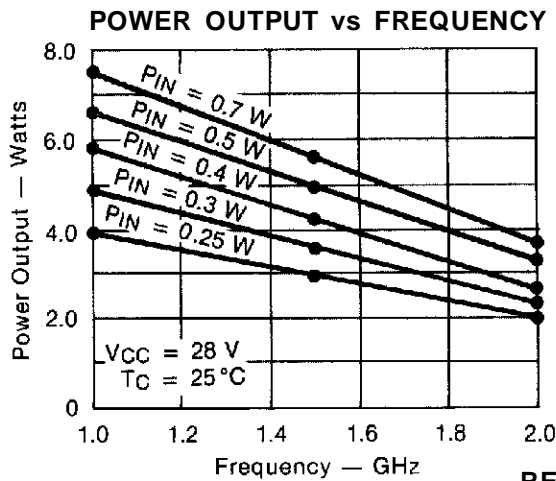
STATIC

Symbol	Test Conditions		Value			Unit
			Min.	Typ.	Max.	
BV _{CBO}	I _C = 1mA	I _E = 0mA	45	—	—	V
BV _{EBO}	I _E = 1mA	I _C = 0mA	3.5	—	—	V
BV _{CER}	I _C = 5mA	R _{BE} = 10Ω	45	—	—	V
I _{CBO}	V _{CB} = 28V		—	—	1.0	mA
h _{FE}	V _{CE} = 5V	I _C = 200mA	15	—	120	—

DYNAMIC

Symbol	Test Conditions			Value			Unit
				Min.	Typ.	Max.	
P _{OUT}	f = 2.0 GHz	P _{IN} = 0.5 W	V _{CC} = 28 V	3.0	3.3	—	W
η _c	f = 2.0 GHz	P _{IN} = 0.5 W	V _{CC} = 28 V	35	37	—	%
G _P	f = 2.0 GHz	P _{IN} = 0.5 W	V _{CC} = 28 V	7.8	8.2	—	dB
C _{OB}	f = 1 MHz	V _{CB} = 28 V		—	—	6.5	pF

TYPICAL PERFORMANCE

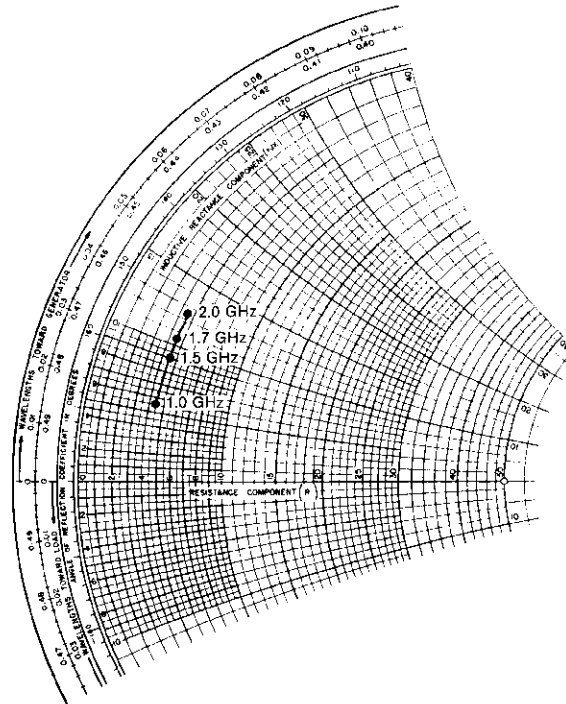


IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE

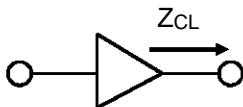


$P_{IN} = 0.5\text{ W}$
 $V_{CC} = 28\text{ V}$
 Normalized to 50 ohms

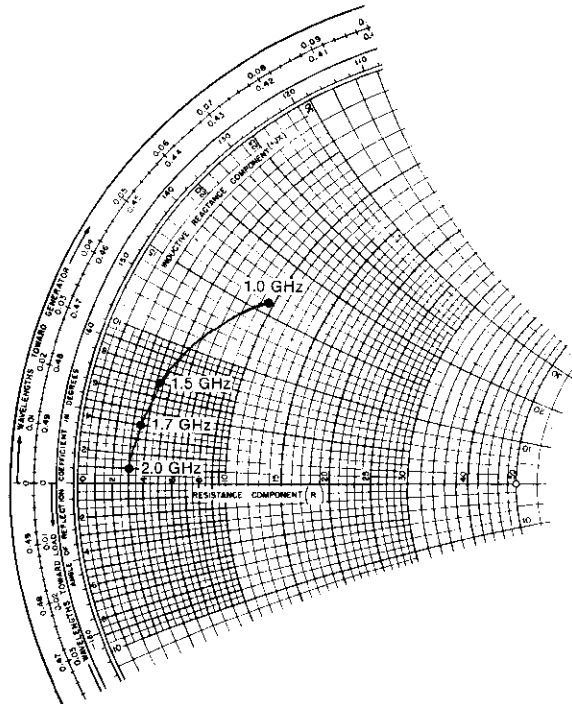


FREQ.	$Z_{IN} (\Omega)$	$Z_{CL} (\Omega)$
1.0 GHz	$4.4 + j 5.5$	$9.6 + j 16.0$
1.5 GHz	$4.5 + j 9.0$	$4.3 + j 7.0$
1.7 GHz	$4.5 + j 10.5$	$3.5 + j 4.0$
2.0 GHz	$4.6 + j 12.5$	$3.0 + j 1.0$

TYPICAL COLLECTOR LOAD IMPEDANCE



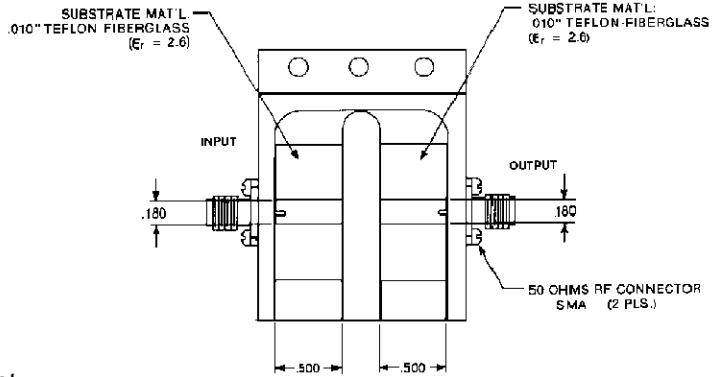
$P_{OUT} = \text{Saturated}$
 $V_{CC} = 28\text{ V}$
 Normalized to 50 ohms



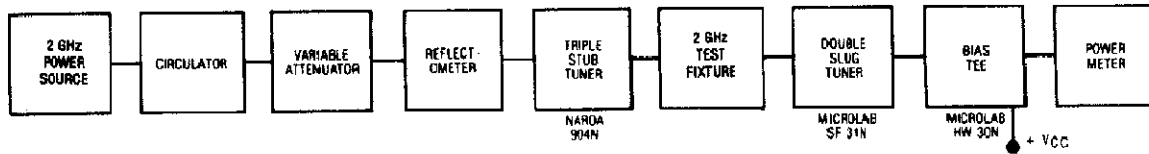
TEST CIRCUIT

Ref.: Dwg. No. C125518

All dimensions are in inches.
Frequency 2.0 GHz

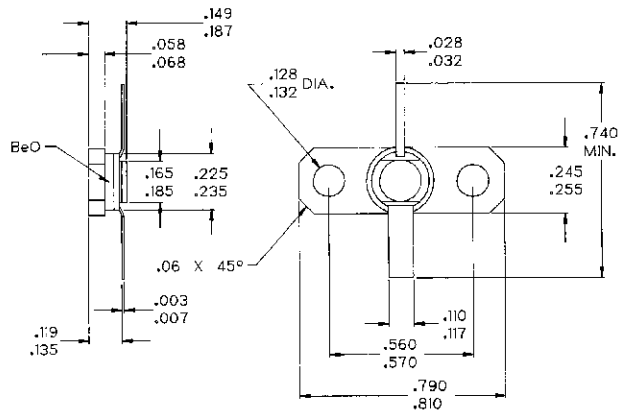


RF Amplifier Power Output Test



PACKAGE MECHANICAL DATA

Ref.: Dwg. No.: J135021C



NOTES:
1. ALL TOLERANCE \pm .010 EXCEPT WHERE NOTED;
DIMENSIONS IN INCHES.

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